

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Craig T. Salling, et al.

Art Unit: 2812

Division of Serial No.: 10/051,962

Examiner: Ron E. Pompey

Filed: Herewith

Docket: TI-32535.1

For: Eliminating Substrate Noise by an Electrically Isolated High-Voltage I/O Transistor

"EXPRESS MAILING" Mailing Label No. EV334469405US. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date shown below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz 10-14-03  
Karen Vertz Date

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Dear Sir:

Prior to the examination of the above-identified application, please amend as follows:

**IN THE CLAIMS:**

Please cancel Claims 1-7.

Should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

Respectfully submitted,

Texas Instruments Incorporated  
P.O. Box 655474 M/S 3999  
Dallas, Texas 75265  
(972) 470-0130

  
Gary C. Honeycutt  
Reg. No. 20,250  
Attorney for Applicants